Claims 1 to 4, 9 to 11, 16, 17, 20 and 21 were rejected under 35 U.S.C. 102(e) as being anticipated by Harms et al. (U.S. 6,493,376). The rejection is respectfully traversed.

The subject application has claimed priority under 35 U.S.C. 119(e)(1) based upon provisional application 60/091,006, filed June 25, 1999, the specification of which is substantially the same as that of the subject application. The patent to Harms et al. bears a filing date of October 9, 1998, which date is subsequent to the effective filing date of the subject application. The provisional application of Harms et al. is not of record since there is no showing on the record that the provisional application of Harms et al. discloses the features claimed in the subject application. Accordingly, Harms et al. is not available as a reference in this application on the basis of the present record.

Claims 5 to 8, 18 and 19 were rejected under 35 U.S.C. 103(a) as being unpatentable over Harms et al. in view of Hendrickson et al. (U.S. 5,974,584). The rejection is respectfully traversed since Harms et al. is not available as a reference for the reasons stated above.

Claims 12 and 13 were rejected under 35 U.S.C. 103(a) as being unpatentable over Harms et al. in view of Lomp et al. (U.S. 5,799,010). The rejection is respectfully traversed since Harms et al. is not available as a reference for the reasons stated above.

The allowability of claim 14 and 15 is noted with appreciation. However, the comments regarding 35 U.S.C. 112, second paragraph, are not understood since there is no rejection on this section of the statute.

In view of the above remarks, favorable reconsideration and allowance are respectfully requested.

Respectfully submitted,

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14. (Amended) A CDMA decoder for decoding a CDMA encoded signal from a desired CDMA channel, comprising:

a receive input for receiving a CDMA encoded signal;

a code generator for generating a predetermined CDMA code for a predetermined CDMA channel that corresponds to an encoded signal encoded with a corresponding CDMA cod transmitted over the predetermined CDMA channel;

a multiply/accumulate device for multiplying said received signal received on said receive input by said predetermined CDMA code word and operating in the analog domain, said multiply/accumulate device operable to accumulate the results of the multiplication operation over a symbol period to provide an analog result; and

a data conversion device for determining if the analog result corresponds to a predetermined digital state and, if so, generating a digital output corresponding to said predetermined digital state;

wherein said multiply/accumulation device comprises:

at least one series leg including first and secnd series connected transistors disposed between a first node and a second reference voltage, said first transistor having the gate thereof connected to the received signal on said receive input and the gate of said second transistor connected to receive said associated code work;

a storage device connected to said first node;

a precharge device for enabling said first node to be precharged to a defined level prior to the initiation of the accumulation operation at the beginning f a symbol period; and

wherein said first and second transistors provide a multiplication operation for said code word and the received signal and said storage device is operable to accumulate the results of the multiplication operation over a symbol period.